



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tetsuya OTSUKI

Group Art Unit: 2818

Application No.: 10/624,680

Examiner: L. TRAN

Filed: July 23, 2003

Docket No.: 116568

For: INTERCONNECT SUBSTRATE, SEMICONDUCTOR DEVICE, METHODS OF  
MANUFACTURING THE SAME, CIRCUIT BOARD, AND ELECTRONIC  
EQUIPMENT

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the August 4, 2005 Office Action, please consider the following:

**Amendments to the Claims** as reflected in the listing of claims; and

**Remarks.**